

### **AMENDMENTS TO THE CLAIMS**

Claims 1-14, 16-18 and 21-23 are pending in the Application. Claim 1 is an independent claim and claims 2-7 and 21 depend therefrom. Claim 8 is an independent claim and claims 9-13 and 22 depend therefrom. Claim 14 is an independent claim and claims 15-18 and 23 depend therefrom. Claims 15, 19 and 20 were previously canceled. Claims 1, 8, 14 and 16 are currently amended.

#### **Listing of Claims:**

This listing of claims will replace all prior versions and listings of claims in the application.

1. (Currently Amended) A system for decoding video data encoded with a particular standard, said system comprising:

a video decoder for decoding the video data encoded with the particular standard,  
wherein the video decoder comprises a master processor;

instruction memory for storing:

a first set of instructions for decoding encoded video data according to a first encoding standard; and

a second set of instructions for decoding encoded video data according to a second encoding standard;

a host processor for providing an indication to the video decoder indicating the particular encoding standard, wherein the video decoder, comprising the master processor, for decoding the video data encoded with the particular standard is discrete from the host processor; and

wherein the video decoder executes the first set of instructions if the indication indicates that the particular encoding standard is the first encoding standard and executes the second set of instructions if the indication indicates that the particular encoding standard is the second encoding standard.

2. (Original) The system of claim 1, wherein the first encoding standard comprises MPEG-2 and the second encoding standard comprises MPEG-4.

3. (Original) The system of claim 1, wherein the instruction memory stores a third set of instructions for decoding encoded video data according to a third encoding standard, and wherein the video decoder executes the third set of instructions if the indication indicates that the particular encoding standard is the third encoding standard.

4. (Original) The system of claim 3, wherein the first encoding standard comprises MPEG-2, the second encoding standard comprises MPEG-4, and the third encoding standard comprises DV-25.

5. (Original) The system of claim 3, wherein the instruction memory stores a fourth set of instructions for decoding the video data in accordance with the first encoding standard, the second encoding standard, and the third encoding standard.

6. (Original) The system of claim 1, further comprising a register for storing the indication from the host processor.

7. (Original) The system of claim 6, wherein the instruction memory stores a fifth set of instructions, wherein execution of the instructions by the host processor cause:

detecting the particular encoding standard; and

writing the indicator to the register.

8. (Currently Amended) A method for decoding video data encoded with a particular standard, said method comprising:

receiving an indication from a host processor by a video decoder indicating the particular encoding standard, wherein the video decoder comprises a master processor and is discrete from the host processor;

executing a first set of instructions if the indication indicates that the particular encoding standard is a first encoding standard; and

executing a second set of instructions if the indication indicates that the particular encoding standard is the second encoding standard.

9. (Original) The method of claim 8, wherein the first encoding standard comprises MPEG-2 and the second encoding standard comprises MPEG-4.

10. (Original) The method of claim 8, further comprising executing the third set of instructions if the indication indicates that the particular encoding standard is the third encoding standard.

11. (Original) The method of claim 10, wherein the first encoding standard comprises MPEG-2, the second encoding standard comprises MPEG-4, and the third encoding standard comprises DV-25.

12. (Original) The method of claim 10, executing a fourth set of instructions for decoding the video data in accordance with the first encoding standard, the second encoding standard, and the third encoding standard.

13. (Original) The method of claim 8, further comprising:

detecting the particular encoding standard; and

writing the indicator to a register.

14. (Currently Amended) A system for decoding video data encoded with a particular standard, said system comprising:

a code memory for instructions; and

a master decoder processor for loading the code memory with a first set of instructions for decoding encoded video data according to a first encoding standard, where the video data is encoded according to the first encoding standard and for loading the code memory with a second set of instructions for decoding encoded video data according to a second encoding standard, wherein the video data is encoded according to the second encoding standard;

wherein the master decoder processor loads the code memory after receiving an indication from a discrete host processor indicating the particular encoding standard.

15. (Canceled)

16. (Currently Amended) The system of claim 14, wherein execution of the first set of instructions by the master decoder processor controls a first plurality of circuits, and execution of the second set of instructions controls a second plurality of circuits.

17. (Original) The system of claim 14, further comprising a slave engine, said slave engine further comprising:

another instruction memory for storing a third set of instructions if the encoding standard is the second encoding standard.

18. (Original) The system of claim 17, wherein the slave engine comprises a third plurality of circuits, wherein the execution of the third set of instructions controls the third plurality of circuits.

19-20. (Canceled)

21. (Previously Presented) The system of claim 1, wherein:

the first set of instructions comprises a common instruction set and a first encoding standard instruction set; and

the second set of instructions comprises the common instruction set and a second encoding standard instruction set.

22. (Previously Presented) The method of claim 8, wherein:

the first set of instructions comprises a common instruction set and a first encoding standard instruction set; and

the second set of instructions comprises the common instruction set and a second encoding standard instruction set.

23. (Previously Presented) The system of claim 14, wherein:

the first set of instructions comprises a common instruction set and a first encoding standard instruction set; and

the second set of instructions comprises the common instruction set and a second encoding standard instruction set.